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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Response to Arguments

1. Applicant's arguments with respect to claims 1-21 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-3, 6, 9, 10, 16, 17 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dobusch et al. (US Patent # 6,850,276), Gowda et al. (US Patent # 6,275,259) and further in view of Yiannoulos (US Patent # 5,982,318).

[Claim 1]

Dobusch et al. teaches a method of correcting erroneous image signals comprising providing a signal that is working value of the gain $V(x-1)$ that is based on the preceding sensor element $P(x-1)$ is used for the following sensor element $P(x)$ from a set of image signals that represent a single capture scene of interest (col. 2 lines 63-67, col. 2 lines 30-62, col. 3 lines 10-22, Abstract, figure 1),

and digitizing an analog signal of a current pixel using said gain $V(x)$ based on the previous value as a reference to derive a digitized signal of said current pixel within said signal range (col. 2 lines 52-54, col. 2 lines 63-67), including limiting said analog signal of said current pixel by said working value of the gain $V(x)$ that is based on the preceding sensor element, said

Art Unit: 2622

analog signal of said current pixel being another image signal from said set of image signals (col. 2 lines 63-67, col. 3 lines 10-22, figures 1, 3).

Dobusch teaches providing gain $V(x-1)$ that is based on the preceding sensor element $P(x-1)$ is used for the following sensor element $P(x)$ but fails to teach providing a high signal and a low signal based on an image signal of a previously processed pixel.

However Gowda teaches providing a high signal and a low signal (V_{max} and V_{min}) based on an image signal of a previously processed pixel (col. 2 line 30-col. 3 line 10, col. 4 lines 14-22, figure 1).

Therefore taking the combined teachings of Dobusch and Gowda, it would be obvious to one skilled in the art at the time of the invention to have been motivated to have a high signal and a low signal based on an image signal of a previously processed pixel into the system of Dobusch as taught in Gowda in order to use the full dynamic range of the A/D converter thereby avoiding the overflow and underflow conditions as taught in Gowda (col. 3 line 50-col. 4 line 5).

Dobusch in view of Gowda fails to teach a multicolor pixel array which has the same single color designation as the previously processed pixel and is separated from the previously processed pixel by at least one pixel in the multicolor pixel array having a different color designation than the current pixel.

However Yiannoulos teaches ramp generator circuits (figure 4) that is an integrated replica of a photo-integrator with a constant current generator connected in parallel with the photo-sensor, and with the photo-sensor blinded to input radiation (col. 8 lines 10-13). These ramp generator circuits are integrated on the same silicon substrate with APS (active pixel array, col. 8 lines 46-col. 8 line 10) and are therefore read as part of pixel array as shown in figure 5.

Art Unit: 2622

These are used to drive a single slope integration A/D converter of an APS imaging array, permits a direct comparison of a photosensor's response current to the current driving the ramp generator (col. 9 lines 11-14). Figure 5 clearly teaches that the ramp generator 20 (comprising a pixel) of the same color (e.g. red) is used to digitize the red pixel 10 by an A/D converter 34 generating an output $N(R)$. It is also clear in figure 5 that the ramp generator pixels 20 are separated from the red pixel 10 by at least one pixel array.

Therefore taking the combined teachings of Dobusch, Gowda and Yiannoulos, it would be obvious to one skilled in the art at the time of the invention to have been motivated to have a multicolor pixel array which has the same single color designation as the previously processed pixel and is separated from the previously processed pixel by at least one pixel in the multicolor pixel array having a different color designation than the current pixel as taught in Yiannoulos so that the current pixel has the same color designation as the previously processed pixel used to digitize the current pixel when used in the system of Dobusch in view of Gowda in order to cancel out all the nonlinearities and offsets that typically distort the output of an APS photo-integrator.

[Claim 2]

Gowda teaches a step of converting said image signal of said previously processed pixel to said high signal and said low signal (figure 1, V_{max} and V_{min}).

[Claim 3]

Gowda teaches wherein said step of converting said image signal of said previously processed pixel includes digital-to-analog converting (figure 1, DAC 110) said image signal of said

Art Unit: 2622

previously processed pixel to said high signal and said low signal (figure 1, V_{max} and V_{min}), wherein said high and low signals are generated as voltages.

[Claims 9, 10]

These are apparatus claims corresponding to method claims 1 and 3 respectively. Therefore they have been analyzed and rejected based upon method claims 1, 3.

[Claim 17]

Dobusch teaches a method of correcting erroneous image signals during analog-to-digital conversion comprising a sensor array of photosensitive pixels (figure 1, pixel P1 to Pn), each of said photosensitive pixels being configured to accumulate an analog image signal when exposed to light (col. 2 lines 30-40) and an analog-to-digital converter unit (combination of 2-5) operatively coupled to said sensor array to receive analog image signals from said photosensitive pixels, said analog-to-digital converter unit comprising a digital-to-analog converter (figure 1, controller 4 will inherently have a DAC that outputs a previous gain $V(x-1)$ that has values 4, 2, 2 etc. based upon the output of A/D converter 3). Dobusch teaches that the value of preceding gain $V(x-1)$ is used for the following sensor to define a working range about said image signal of said previously processed pixel that represent a single capture scene of interest (col. 2 lines 63-67, col. 2 lines 30-62, col. 3 lines 10-22, Abstract, figure 1),

an analog-to-digital converter (figure 1, ADC 3), said analog-to digital converter being configured to digitize an analog signal of a current pixel (output of pixel array P1 to Pn) using said preceding gain $V(x-1)$ as references to derive a digitized signal of said current pixel within said signal range, including limiting said analog signal of said current pixel by said high and low signals col. 2 lines 63-67, col. 3 lines 10-22, figures 1, 3).

Dobusch teaches providing gain $V(x-1)$ that is based on the preceding sensor element $P(x-1)$ is used for the following sensor element $P(x)$ but fails to teach providing a high signal and a low signal based on an image signal of a previously processed pixel.

However Gowda teaches providing a high signal and a low signal (V_{max} and V_{min}) based on an image signal of a previously processed pixel (col. 2 line 30-col. 3 line 10, col. 4 lines 14-22, figure 1).

Therefore taking the combined teachings of Dobusch and Gowda, it would be obvious to one skilled in the art at the time of the invention to have been motivated to have a high signal and a low signal based on an image signal of a previously processed pixel into the system of Dobusch as taught in Gowda in order to use the full dynamic range of the A/D converter thereby avoiding the overflow and underflow conditions as taught in Gowda (col. 3 line 50-col. 4 line 5).

Dobusch in view of Gowda fails to teach a multicolor pixel array which has the same single color designation as the previously processed pixel and is separated from the previously processed pixel by at least one pixel in the multicolor pixel array having a different color designation than the current pixel.

However Yiannoulos teaches ramp generator circuits (figure 4) that is an integrated replica of a photo-integrator with a constant current generator connected in parallel with the photo-sensor, and with the photo-sensor blinded to input radiation (col. 8 lines 10-13). These ramp generator circuits are integrated on the same silicon substrate with APS (active pixel array, col. 8 lines 46-col. 8 line 10) and are therefore read as part of pixel array as shown in figure 5. These are used to drive a single slope integration A/D converter of an APS imaging array, permits a direct comparison of a photosensor's response current to the current driving the ramp

Art Unit: 2622

generator (col. 9 lines 11-14). Figure 5 clearly teaches that the ramp generator 20 (comprising a pixel) of the same color (e.g. red) is used to digitize the red pixel 10 by an A/D converter 34 generating an output $N(R)$. It is also clear in figure 5 that the ramp generator pixels 20 are separated from the red pixel 10 by at least one pixel array.

Therefore taking the combined teachings of Dobusch, Gowda and Yiannoulos, it would be obvious to one skilled in the art at the time of the invention to have been motivated to have a multicolor pixel array which has the same single color designation as the previously processed pixel and is separated from the previously processed pixel by at least one pixel in the multicolor pixel array having a different color designation than the current pixel as taught in Yiannoulos so that the current pixel has the same color designation as the previously processed pixel used to digitize the current pixel when used in the system of Dobusch in view of Gowda in order to cancel out all the nonlinearities and offsets that typically distort the output of an APS photo-integrator.

[Claim 6]

Dobusch in view of Gowda is silent as to the type of analog-to-digital converter, however Official notice is taken of the fact that it is notoriously common to have a flash analog-to-digital converter be used for digitizing a current pixel in order to make the overall process faster. Therefore taking the combined teachings of Dobusch, Gowda and Official notice, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have flash analog-to-digital converter be used for digitizing a current pixel. The benefit of doing so would be because flash A/Ds have high input bandwidth and very high speeds in the 1 to 4-

Art Unit: 2622

Gsample/s range. [As applicant has not traversed the old and well known statement above, the use of a flash analog-to-digital converter is taken as admitted prior art. See MPEP 2144.03(c)]

[Claim 16]

This is an apparatus claim corresponding to method claim 6. Therefore it has been analyzed and rejected based upon method claim 6.

[Claim 21]

This claim is substantially similar to claim 16. Therefore it has been analyzed and rejected based upon claim 16.

4. Claims 4, 5, 8, 11-14, 18,19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dobusch et al. (US Patent # 6,850,276), Gowda et al. (US Patent # 6,275,259), Yiannoulos (US Patent # 5,982,318) and in further view of Kim (US Patent # 6,587,144).

[Claims 4 and 5]

Dobusch, Gowda in view of Yiannoulos teaches all the limitations of claim 1. Furthermore, Dobusch teaches that is working value of the gain $V(x-1)$ that is based on the preceding sensor element $P(x-1)$ is used for the following sensor element $P(x)$ from a set of image signals that represent a single capture scene of interest (col. 2 lines 63-67, col. 2 lines 30-62, col. 3 lines 10-22, Abstract, figure 1), but fails to teach “.... Wherein a step of comparing said analog signal of said current pixel with an analog signal of a previously processed pixel and further comprising a step of converting said image signal of said previously processed pixel to said high signal and said low signal, wherein said high and low signals are dependent on said comparing of said analog signal of said current pixel with said analog signal of said previously processed pixel”.

Art Unit: 2622

However Kim teaches comparing (figure 1, element 42) a present black level signal (read as current pixel signal value) and a preset black reference value (read as previously processed pixel value) to up or down values so that the DC voltage level of the signal is adjusted (col. 2 lines 12-23)[DC voltage can be either high or low and therefore can be read as high and low signals which are dependent on the comparison between a present black level and preset black reference value].

Therefore taking the combined teachings of Dobusch, Gowda, Yiannoulos and Kim, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have comparing said analog signal of said current pixel with an analog signal of a previously processed pixel and further comprising a step of converting said image signal of said previously processed pixel to said high signal and said low signal, wherein said high and low signals are dependent on said comparing of said analog signal of said current pixel with said analog signal of said previously processed pixel. The benefit of doing so would be to correct the black level due to an incorrect pixel as taught in Kim (col. 2 lines 20-21).

[Claim 8]

Dobusch, Gowda in view of Yiannoulos teaches all the limitations of claim 1. Furthermore, Gowda teaches wherein said image signal of said previously processed pixel is a digital signal (output of ADC 104) but fails to teach “...., wherein said image signal has more bits than said digitized signal of said current pixel”.

However Kim teaches that the A/d converter output has 10 bits as compared to a 6-bit black level reference value (col. 4 lines 25-30).

Art Unit: 2622

Therefore taking the combined teachings of Dobusch, Gowda, Yiannoulos and Kim, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have an image signal having more bits than said digitized signal of said current pixel. The benefit of doing so would be to vary the black reference value as needed as taught in Kim (col. 4 lines 25-26).

[Claim 11]

This is an apparatus claim corresponding to method claim 8. Therefore it has been analyzed and rejected based upon method claim 8.

[Claim 12]

Dobusch, Gowda, Yiannoulos in view of Kim teaches all the limitations of claim 11.

Furthermore, Gowda teaches a 8-bit D/A and A/d converter but does not disclose a 10-bit D/A and 7-bit A/D converter. However Official notice is taken of the fact that a 10 bit D/A and 7-bit A/D converter is well known in the art in order to have more sensitivity. Therefore taking the combined teachings of Dobusch, Gowda, Yiannoulos, Kim and Official notice, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have used a seven-bit value. The benefit of doing so would be to have a circuit, which has high sensitivity. [As applicant has not traversed the old and well known statement above, the use of a 10 bit D/A and 7-bit A/D converter is taken as admitted prior art. See MPEP 2144.03(c)]

[Claim 13]

This is an apparatus claim corresponding to method claims 4 and 5. Therefore it has been analyzed and rejected based upon method claims 4 and 5.

[Claim 14]

Art Unit: 2622

Claim 14 recites what was discussed with respect to claim 12.

[Claim 18]

This claim is substantially similar to claim 11. Therefore it has been analyzed and rejected based upon claim 11.

[Claim 19]

This claim is substantially similar to claim 13. Therefore it has been analyzed and rejected based upon claim 13.

5. Claims 7, 15, 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dobusch et al. (US Patent # 6,850,276), Gowda et al. (US Patent # 6,275,259), Yiannoulos (US Patent # 5,982,318) in view of Embler (US Patent # 6,654,054).

[Claim 7]

Dobusch, Gowda in view of Yiannoulos teaches all the limitations of claim 1. Furthermore, Gowda teaches that the digitized signal is based upon the previously processed pixel as discussed in claim 1 but fails to teach "... a step of adding a conversion signal to said digitized signal of said current pixel". However Embler teaches that an anti-noise signal is added to the digital signal (col. 11 lines 32-38). Therefore taking the combined teachings of Dobusch, Gowda, Yiannoulos and Embler, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have a step of adding a conversion signal to said digitized signal of said current pixel. The benefit of doing so would be to ensure that an appropriate noise signal is cancelled as taught in Embler (col. 11 lines 32-38).

[Claim 15]

This is an apparatus claim corresponding to method claim 7. Therefore it has been analyzed and rejected based upon method claim 7.

[Claim 20]

This claim is substantially similar to claim 15. Therefore it has been analyzed and rejected based upon claim 15.

Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to YOGESH K. AGGARWAL whose telephone number is (571)272-7360. The examiner can normally be reached on M-F 9:00AM-5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lin Ye can be reached on (571)-272-7372. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Nhan T. Tran/
Primary Examiner, Art Unit 2622